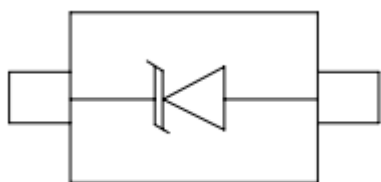




Description

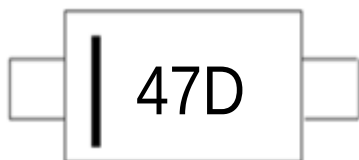
The JLS05UGS3-2 is an Uni-directional high power TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLS05UGS3-2 complies with the IEC 61000-4-2 (ESD) with $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. It is assembled into an ultra-small lead-free SOD-323 package. The small size and high ESD surge protection make JLS05UGS3-2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

47D: Device Marking Code

Features

- * 2200W peak pulse power (8/20 μs)
- * Low leakage: μA level
- * Operating voltage: 5V
- * Low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC 61000-4-5 (Lightning) 150A (8/20 μs)
- * RoHS Compliant
- * Package: SOD-323

Applications

- * Mobile Phones and Accessories
- * Power Supply Protection
- * Hand Held Portable Applications

Ordering Information

Part Number	Packaging	Reel Size
JLS05UGS3-2	3000/Tape & Reel	7 inch

**Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)**

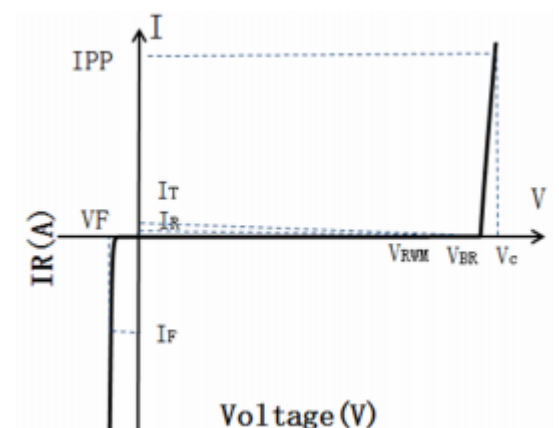
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	2200	W
Peak Pulse Current (8/20 μs)	IPP	150	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				5	V
Breakdown Voltage	V_{BR}	$I_{\text{T}} = 1\text{mA}$	6			V
Reverse Leakage Current	I_{R}	$V_{\text{RWM}} = 5\text{V}$			1	μA
Clamping Voltage	V_{C}	$I_{\text{PP}} = 20\text{A}$ (8 x 20 μs pulse)			9.5	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 150\text{A}$ (8 x 20 μs pulse)			15	V
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$,)		450		pF

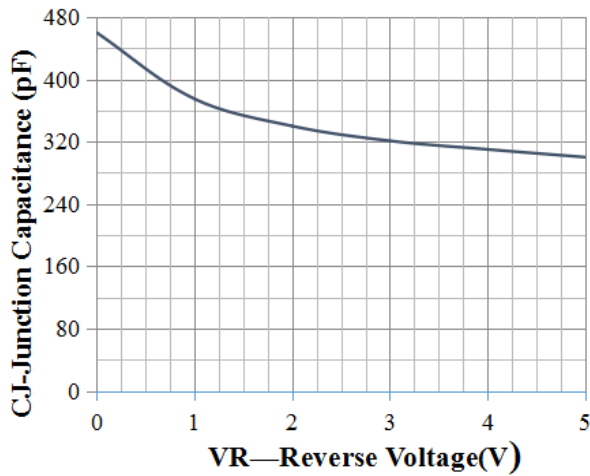
Portion Electronics Parameter

Symbol	Parameter
I_{T}	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_{C}	Clamping Voltage @ I_{C}

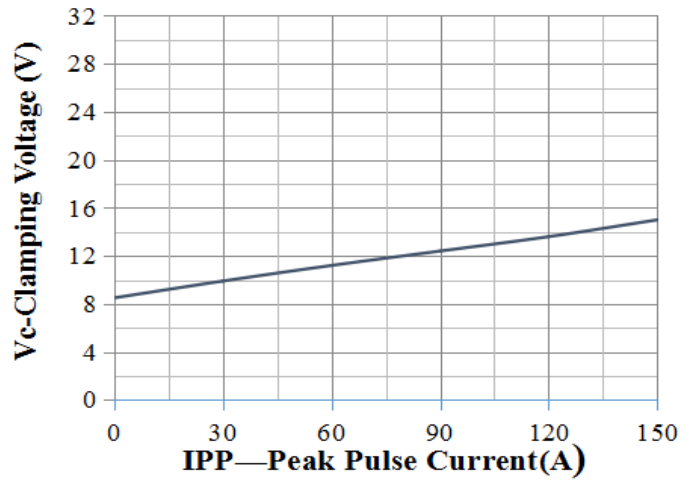




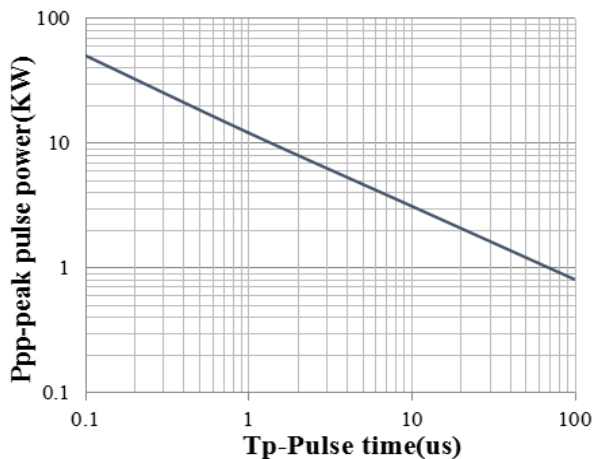
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



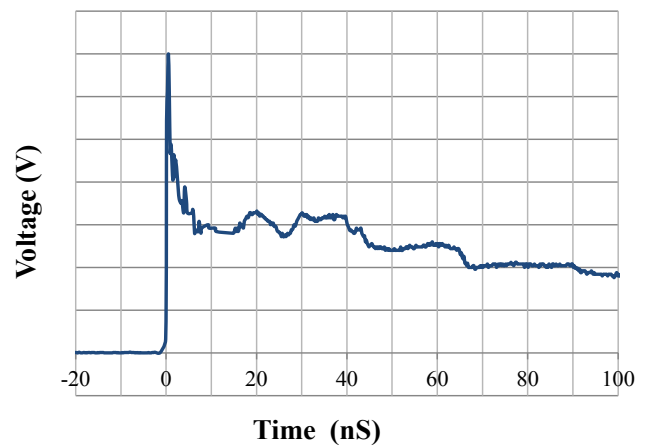
Junction Capacitance vs. Reverse Voltage



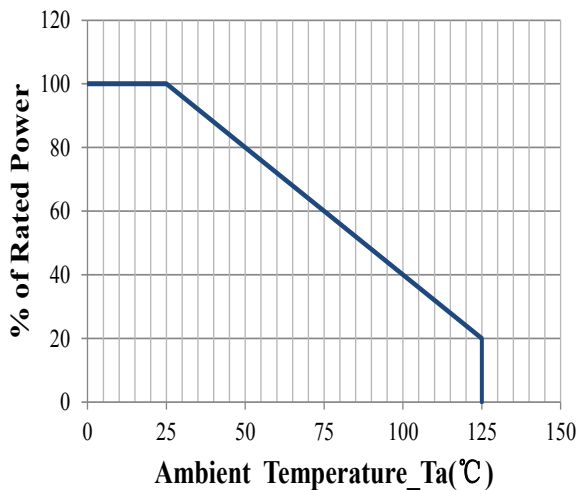
Clamping Voltage vs. Peak Pulse Current



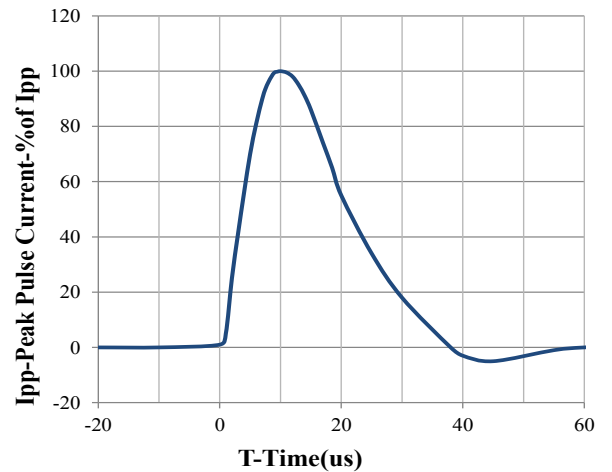
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

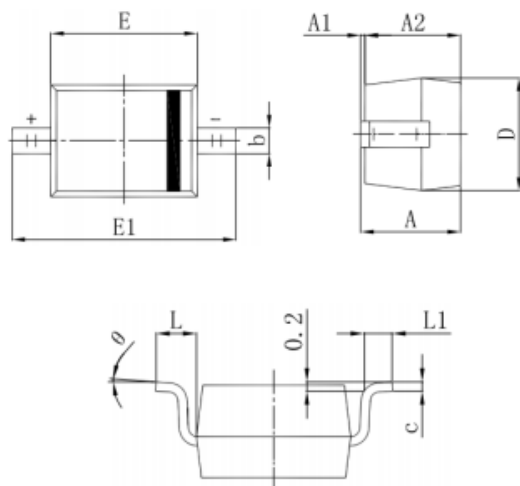


8 X 20us Pulse Waveform



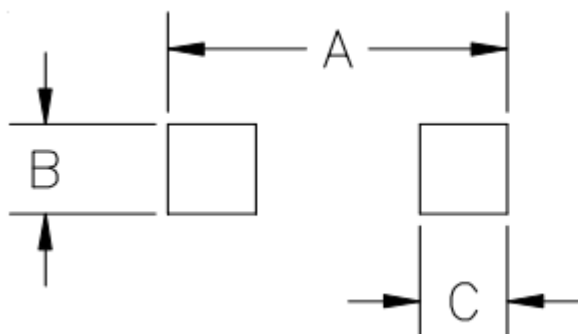
JLS05UGS3-2

SOD-323 Package Outline Drawing (Dimensions in millimeters)



SYM	DIMENSIONS			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A		1.00		0.039
A1	0.00	0.10	0.000	0.004
A2	0.80	0.90	0.031	0.035
b	0.25	0.35	0.010	0.014
c	0.08	0.15	0.003	0.006
D	1.20	1.40	0.047	0.055
E	1.60	1.80	0.063	0.071
E1	2.50	2.75	0.098	0.108
L	0.475REF		0.019REF	
L1	0.25	0.40	0.010	0.016
Θ	0°	8°	0°	8°

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
A	3.15	0.120
B	0.80	0.031
C	0.80	0.031

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