

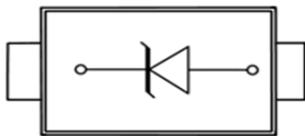


Description

The JLS07UGS3-2 is a uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLS07UGS3-2 complies with the IEC 61000-4-2 (ESD) standard with $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. It is assembled into an ultra-small lead-free SOD-323 package.

The small size and high ESD surge protection make JLS07UGS3-2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit Diagram

Marking Diagram



Transparent top view

77D:Device Marking Code

Features

- * Protects one data or power line
- * Ultra low leakage: nA level
- * Operating voltage: 7V
- * Low clamping voltage
- * 2-pin leadless package
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-5 (Lightning) 100A (8/20 μs)
- * RoHS Compliant
- * Package: SOD-323

Applications

- * Mobile Phones
- * Battery Protection
- * Power Line Protection
- * Vbat pin for Mobile Devices
- * Hand Held Portable Applications

Ordering Information

Part Number	Packaging	Reel Size
JLS07UGS3-2	3000/Tape & Reel	7 inch

**Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)**

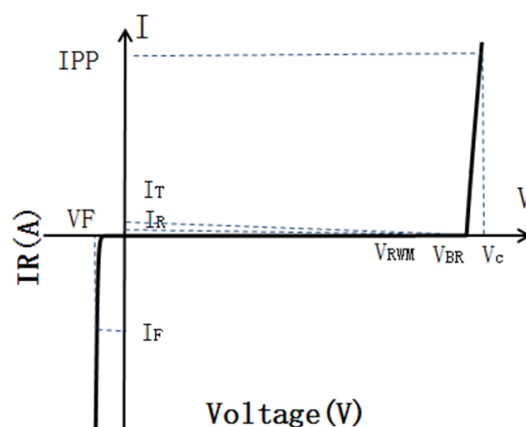
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	2200	W
Peak Pulse Current (8/20 μs)	IPP	100	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				7.0	V
Breakdown Voltage	V_{BR}	$I_{\text{T}} = 1\text{mA}$	8.0	9.0	10.0	V
Reverse Leakage Current	I_{R}	$V_{\text{RWM}} = 7.0\text{V}$			0.5	μA
Clamping Voltage	V_{C}	$I_{\text{PP}} = 1\text{A}$ (8 x 20 μs pulse)			8.5	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 100\text{A}$ (8 x 20 μs pulse)			22	V
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$		430	460	pF

Portion Electronics Parameter

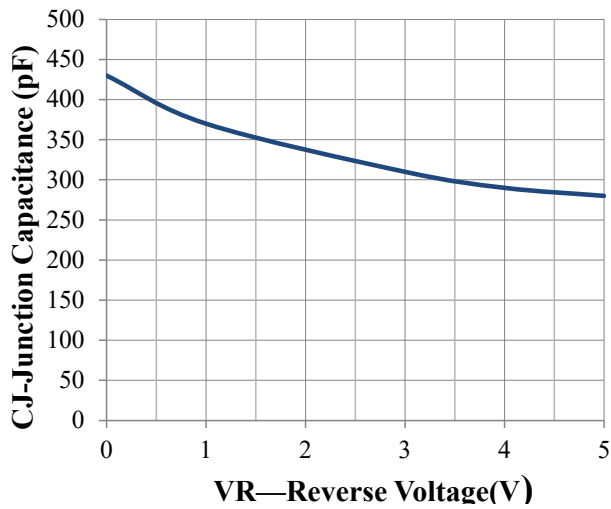
Symbol	Parameter
I_{T}	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_{C}	Clamping Voltage @ I_{C}



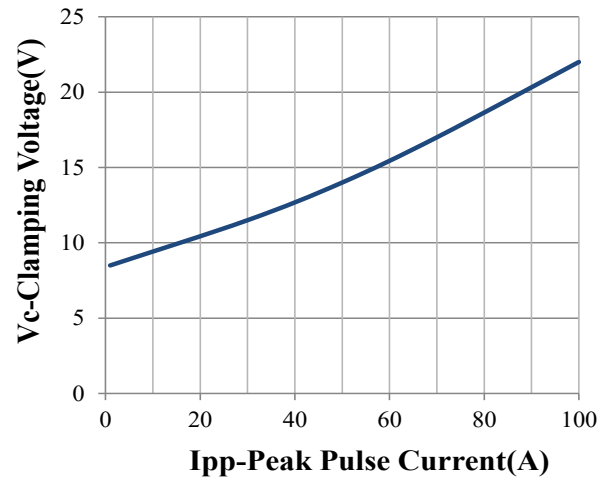


JLS07UGS3-2

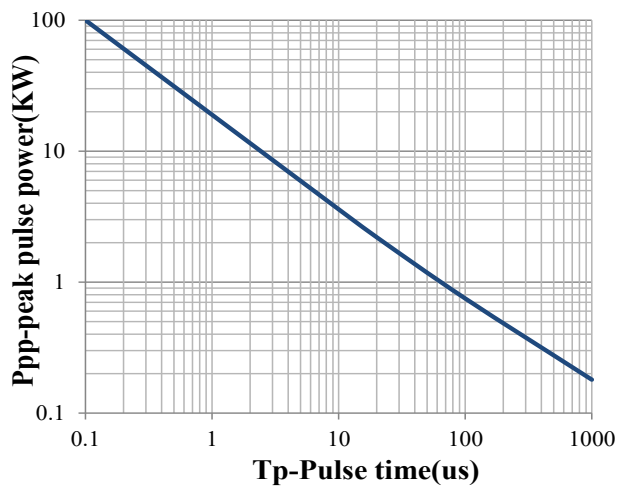
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



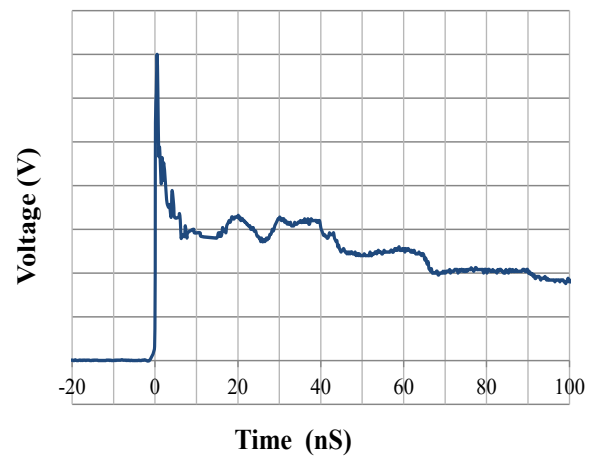
Junction Capacitance vs. Reverse Voltage



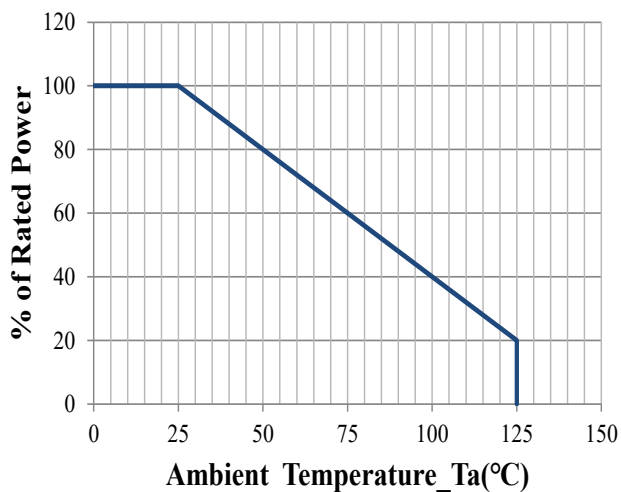
Clamping Voltage vs. Peak Pulse Current



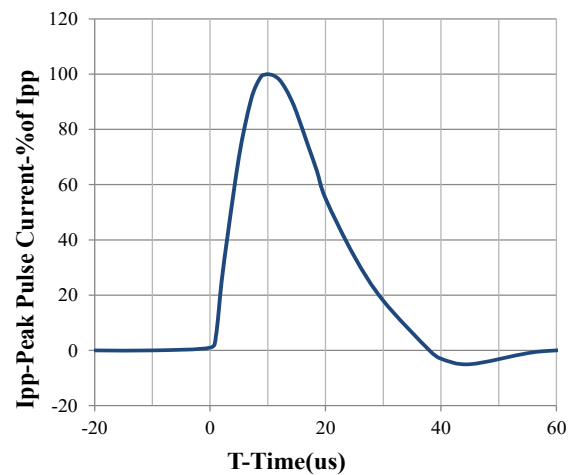
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



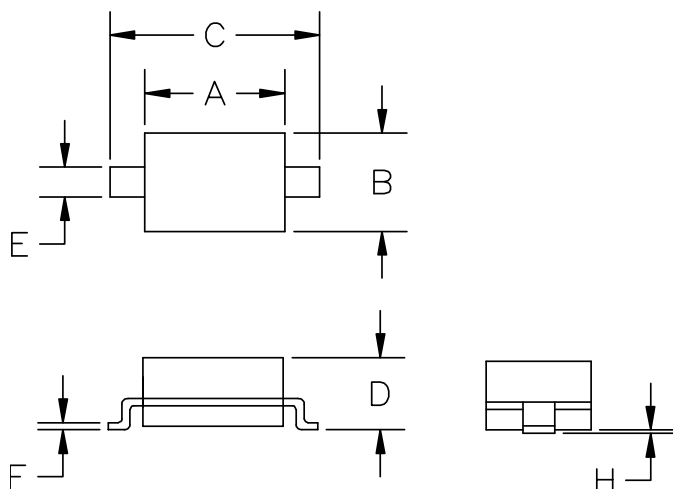
Power Derating Curve



8 X 20us Pulse Waveform

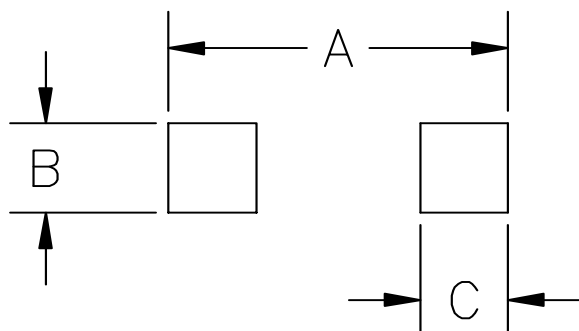


SOD-323 Package Outline Drawing



SYM	DIMENSIONS			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.50	1.80	0.060	0.071
B	1.20	1.40	0.045	0.054
C	2.30	2.70	0.090	0.107
D	-	1.10	-	0.043
E	0.30	0.40	0.012	0.016
F	0.10	0.25	0.004	0.010
H	-	0.10	-	0.004

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
A	3.15	0.120
B	0.80	0.031
C	0.80	0.031

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