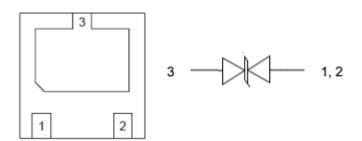


### Description

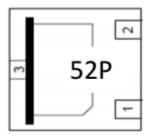
The JLS05BGD5-3 is a high power TVS, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive lines. The JLS05BGD5-3 complies with the IEC 61000-4-2 (ESD) with ±30kV air and ±30kV contact discharge. It is assem- bled into a 3-pin DFN2020-3 lead-free package. The leads are finished with NiPdAu. Each device will protect one line. The combination of small size, and high surge capability makes them ideal for use in applications such as cellular phones, LCD displays, USB, and multi media card interfaces.

### **Circuit Diagram**



Circuit and Pin Schematic

### **Marking Diagram**



**Transparent top view** 52P:Device Marking Code

### Features

- \* 6800W peak pulse power (8/20µs)
- \* Low leakage: nA
- \* Operating voltage: 5V
- \* Ultra low clamping voltage
- \* One power line protects
- \* Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 310A (8/20µs)
- RoHS Compliant
- \* Package: DFN2020-3

#### **Applications**

- \* Fast-charge battery chargers
- \* Power management system
- \* Cellular Handsets and Accessories
- Personal Digital Assistants
- \* Notebooks and Handhelds
- \* Portable Instrumentation
- \* Digital Cameras

# **Ordering Information**

Part Number	Packaging	<b>Reel Size</b>
JLS05BGD5-3	3000/Tape & Reel	7 inch



# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

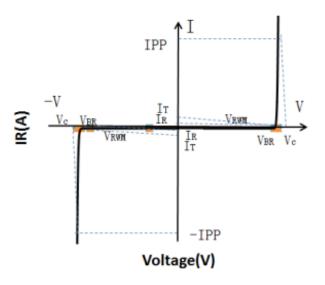
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20µs)	Ppk	6800	W	
Peak Pulse Current (8/20µs)	IPP	310	А	
ESD per IEC 61000-4-2 (Air)	VECD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	$\pm 30$		
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				5	V
Breakdown Voltage	VBR	$I_T = 1mA$	6			V
Reverse Leakage Current	I <sub>R</sub>	$V_{RWM} = 5V$			0.5	uA
Clamping Voltage	Vc	$I_{PP} = 50A (8 \times 20 \mu s \text{ pulse})$			9	V
Clamping Voltage	Vc	$I_{PP} = 310A (8 \times 20 \mu s \text{ pulse})$			22	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz		530		pF

# **Portion Electronics Parameter**

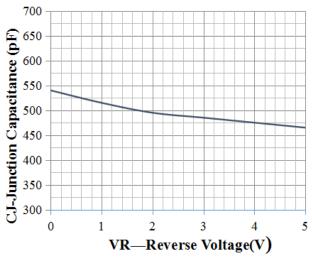
Symbol	Parameter
Ιτ	Test Current
Ірр	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @Ic



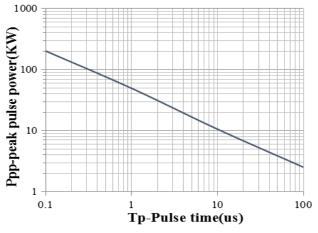


# JLS05BGD5-3

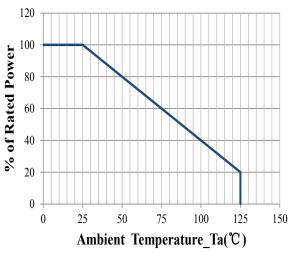
# Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise Specified)



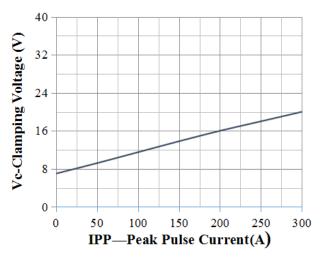
Junction Capacitance vs. Reverse Voltage



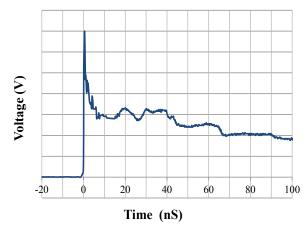
Peak Pulse Power vs. Pulse Time



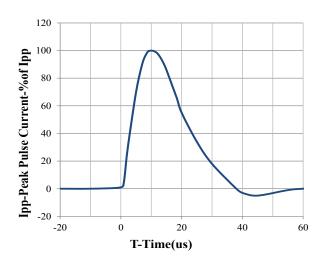
**Power Derating Curve** 



**Clamping Voltage vs. Peak Pulse Current** 



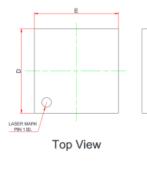
IEC61000-4-2 Pulse Waveform



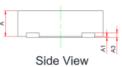
8 X 20us Pulse Waveform



### DFN2020-3 Package Outline Drawing (Dimensions in millimeters)







× 1 1 1		-	R (↑		
	-	0			
	b				
Bottom View					

A3	0.10REF.			
b	0.25		0.35	
D	1.90		2.10	
E	1.90		2.10	
D2	0.95		1.15	
E2	1.40		1.60	
е	1.20		1.40	
н	0.20		0.30	
к	0.20		0.40	
L	0.35		0.45	
R	0.13			

MIN

0.55

0.00

SYM А

A1

MILLIMETERS

NOM

0.60

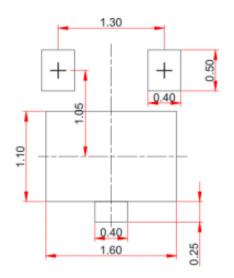
0.02

MAX

0.65

0.05

### **Suggested Land Pattern**



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