



JLS45UGD5-3

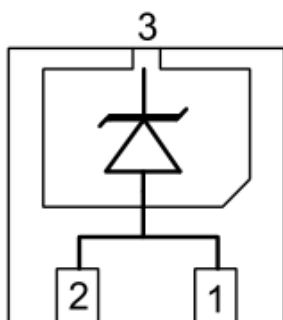
1-Line Uni-directional High Power TVS Diode

Jialan-Microelectronics

Description

The JLS45UGD5-3 is a high power TVS, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive lines.. It is assembled into a 3-pin DFN2020-3 lead-free package. The leads are finished with NiPdAu. Each device will protect one line. The combination of small size, and high surge capability makes them ideal for use in applications such as cellular phones, LCD displays, USB, and multi media card interfaces.

Circuit Diagram



Transparent bottom view

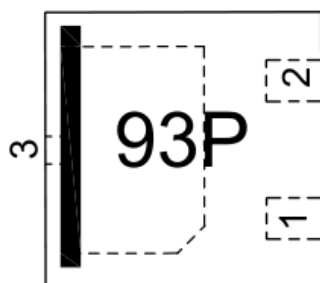
Features

- * 6000W peak pulse power (8/20 μ s)
- * Low leakage: μ A level
- * Operating voltage: 4.5V
- * Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: ± 30 kV
 - Contact discharge: ± 30 kV
 - IEC61000-4-5 (Lightning) 300A (8/20 μ s)
- * RoHS Compliant
- * Package: DFN2020-3
- * Lead Finish: NiPdAu

Applications

- * Power Management
- * Industrial Application
- * Power Supply Protection
- * Many other portable devices

Marking Diagram



Transparent top view

93P:Device Marking Code

Bar denotes cathode

Ordering Information

Part Number	Packaging	Reel Size
JLS45UGD5-3	3000/Tape & Reel	7 inch



JLS45UGD5-3

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

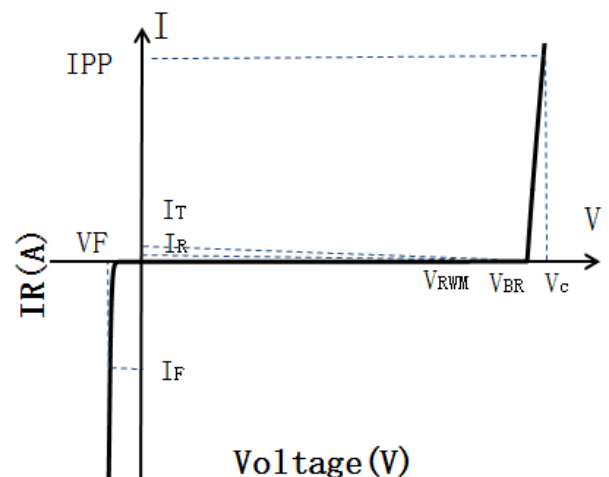
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	6000	W
Peak Pulse Current (8/20 μs)	IPP	300	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				4.5	V
Breakdown Voltage	V_{BR}	$I_{\text{T}} = 1\text{mA}$	4.8			V
Reverse Leakage Current	I_{R}	$V_{\text{RWM}} = 4.5\text{V}$			15.0	μA
Clamping Voltage	V_{C}	$I_{\text{PP}} = 150\text{A}$ (8 x 20 μs pulse)			14.0	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 300\text{A}$ (8 x 20 μs pulse)			22.0	V
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$		700	800	pF

Portion Electronics Parameter

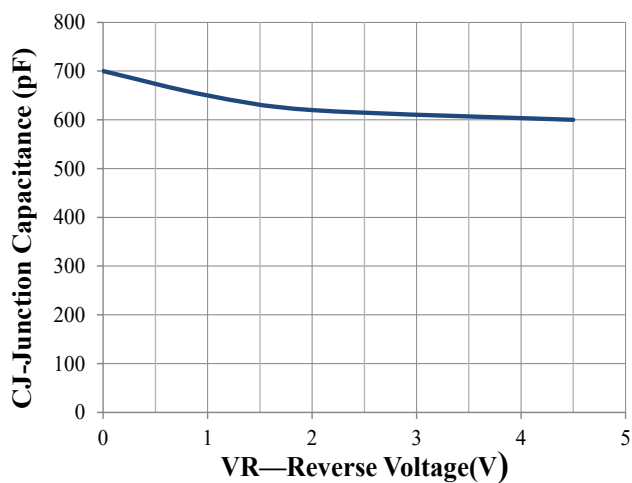
Symbol	Parameter
I_{T}	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_{C}	Clamping Voltage @ I_{C}
I_{F}	Forward Current
V_{F}	Forward Voltage @ I_{F}



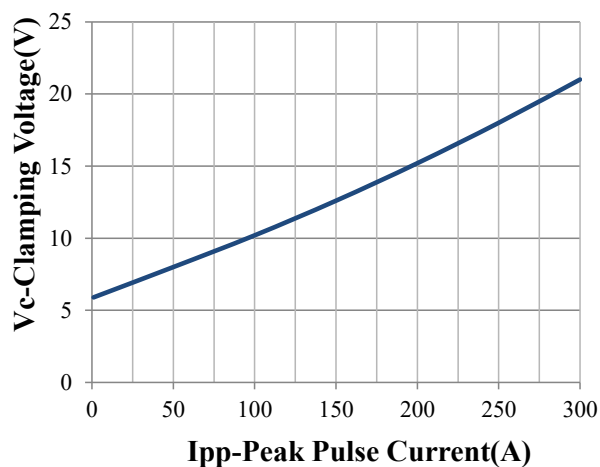


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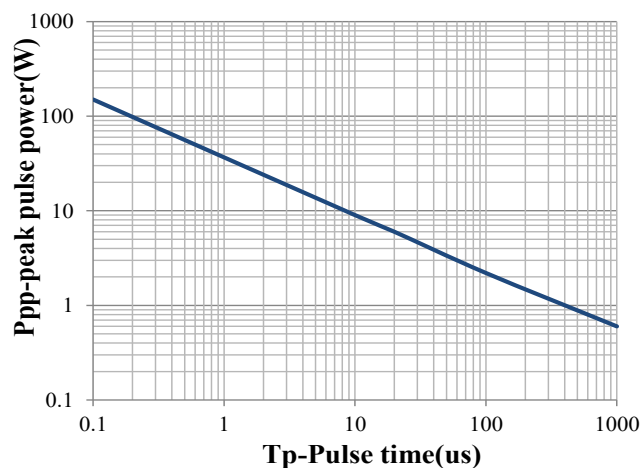
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



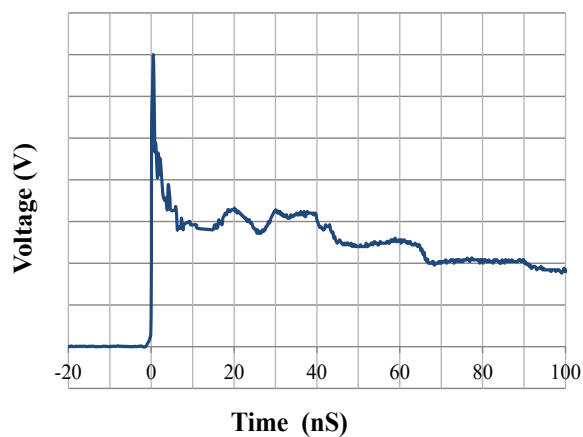
Junction Capacitance vs. Reverse Voltage



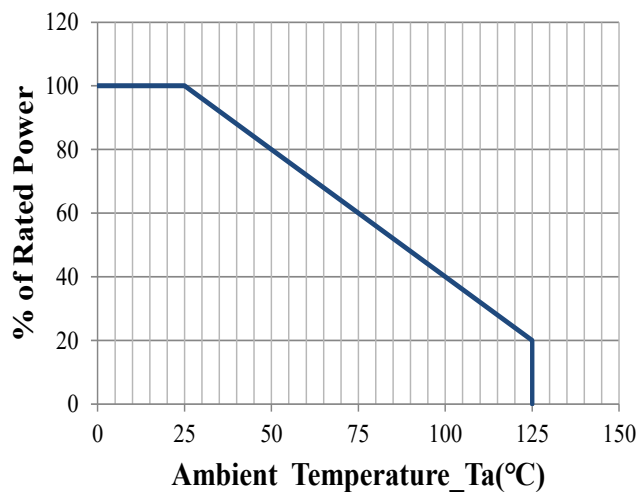
Clamping Voltage vs. Peak Pulse Current



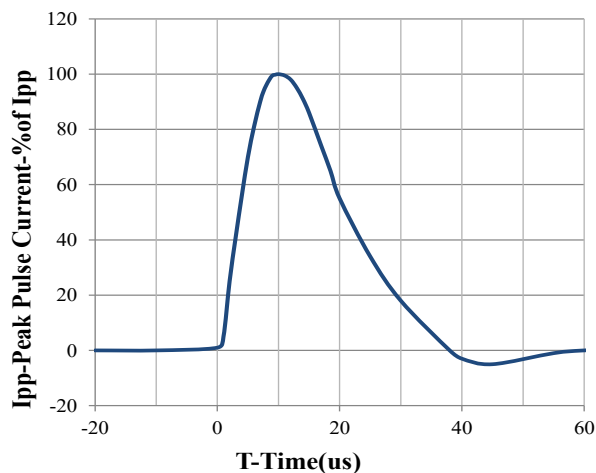
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

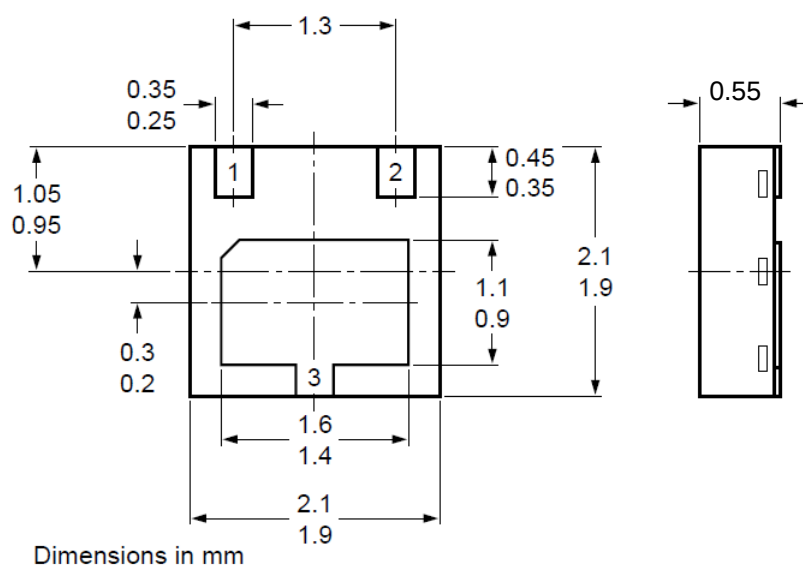


8 X 20us Pulse Waveform

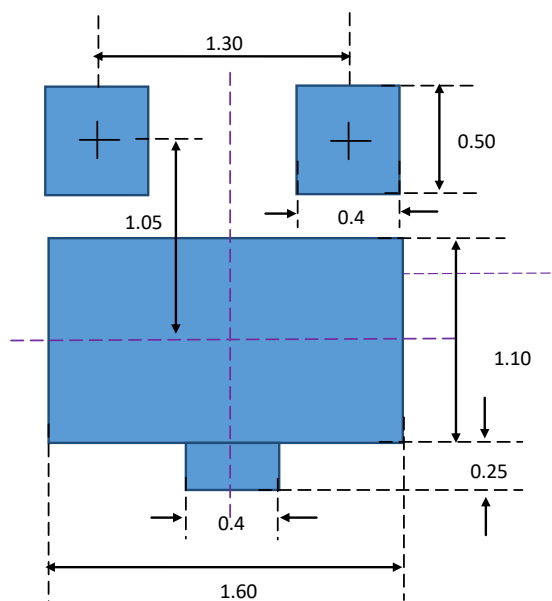


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DFN2020-3 Package Outline Drawing



Suggested Land Pattern



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