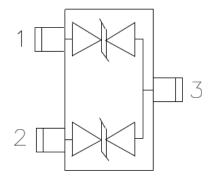
### **Description**

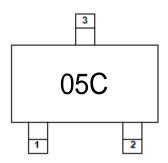
The JLE05BLT1-3 is a bi-directional TVS diode array, utilizing leading monolithic silicon technology to provide fast re- sponse time and low ESD clamping voltage, making this device an ideal solution for protecting sensitive semicon- ductor components from damage. The JLE05BLT1-3 com- plies with the IEC 61000 -4-2 (ESD) with  $\pm 30 \rm kV$  air and  $\pm 30 \rm kV$  contact discharge. It is assembled into a lead-free SOT-23 package. It is designed to protect components which are connected to data and transmission lines from voltage surges.

### **Circuit Diagram**



Circuit and Pin Schematic

## **Marking Diagram**



Transparent top view

05C:Device Marking Code

#### **Features**

- \* 300W peak pulse power (8/20µs)
- Low leakage: nA level
- Low operating voltage: 5V
- \* Ultra low clamping voltage
- One power line protects
- \* Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 20A (8/20μs)
- \* RoHS Compliant
- Package: SOT-23

### **Applications**

- \* Fast-charge battery chargers
- \* Power management system
- \* Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- \* Portable Instrumentation
- \* Digital Cameras

### **Ordering Information**

Part Number	Packaging	Reel Size
JLE05BLT1-3	3000/Tape & Reel	7 inch



# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

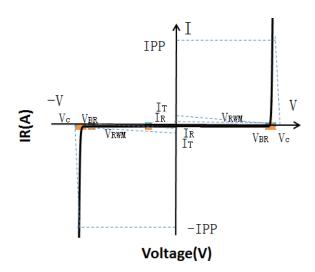
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20μs)	Ppk	300	W	
Peak Pulse Current (8/20μs)	IPP	20	A	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30	K V	
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	VRWM				5	V
Breakdown Voltage	VBR	$I_T = 1 \text{mA}$	6			V
Reverse Leakage Current	$I_R$	$V_{RWM} = 5V$			1.0	μΑ
Clamping Voltage	Vc	$I_{PP} = 1A (8 \times 20 \mu s \text{ pulse})$			8	V
Clamping Voltage	Vc	$I_{PP} = 20A (8 \times 20 \mu s \text{ pulse})$			15	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz, Pin 1to Pin 3 or Pin 2 to Pin3		80		pF

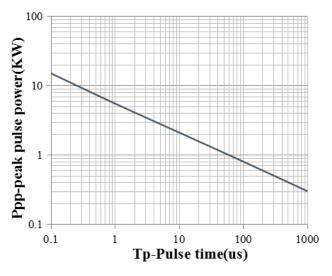
### **Portion Electronics Parameter**

Symbol	Parameter	
Iτ	Test Current	
Ірр	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @Ic	

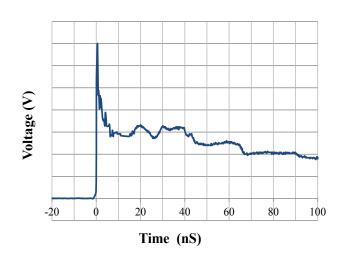




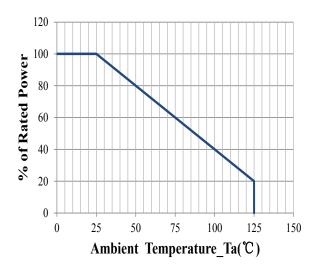
## Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise Specified)



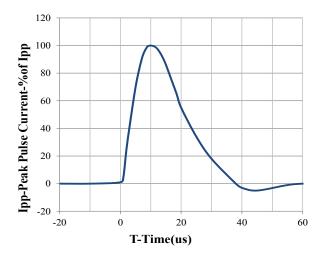
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



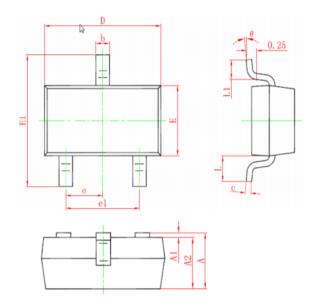
**Power Derating Curve** 



8 X 20us Pulse Waveform

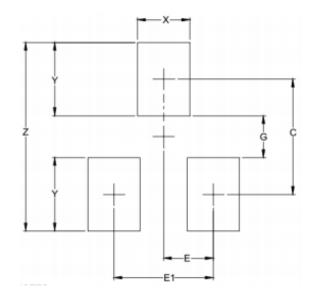


### **SOT-23 Package Outline Drawing**



		DIMENSIONS				
	MILLIMETERS			INCHES		
SYM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90		1.15	0.035		0.045
A1	0.00		0.10	0.000		0.004
A2	0.90		1.05	0.035		0.041
b	0.30		0.50	0.012		0.020
С	0.08		0.15	0.003		0.006
D	2.80		3.00	0.110		0.118
E	1.20		1.40	0.047		0.055
E1	2.25		2.25	0.089		0.100
е		0.95TYP			0.037TYP	
e1	1.80		2.00	0.071		0.079
L	0.55REF				0.022REF	
L1	0.30		0.50	0.012		0.020
Θ	0°		8°	0°		8°

### **Suggested Land Pattern**



CVM	DIMENSIONS			
SYM	INCHES	MILLIMETERS		
С	.087	2.20		
E	.037	0.95		
E1	.075	1.90		
G	.031	0.80		
Х	.039	1.00		
Υ	.055	1.40		
Z	.141	3.60		

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