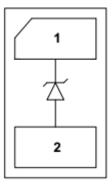


### Description

The JLE15ULD2-2 is an uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast re- sponse time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE15ULD2-2 complies with the IEC 61000-4-2 (ESD) with  $\pm$ 30kV air and  $\pm$ 30kV contact discharge. It is assembled into an ultra-small 1.0x0.6x0.5mm lead-free DFN package. The small size and high ESD surge protection make JLE15ULD2-2 an

ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

### **Circuit Diagram**



Circuit and Pin Schematic

### **Marking Diagram**



**Transparent top view** FS:Device Marking Code

### Features

- \* 320W peak pulse power (8/20µs)
- \* Low leakage:nA level
- \* Operating voltage: 15V
- \* Low clamping voltage
- \* One power line protects
- \* Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 10A (8/20µs)
- \* RoHS Compliant
- Package: DFN1006-2

#### **Applications**

- \* Fast-charge battery chargers
- \* Power management system
- \* Cellular Handsets and Accessories
- Personal Digital Assistants
- \* Notebooks and Handhelds
- \* Portable Instrumentation
- \* Digital Cameras

# **Ordering Information**

Part Number	Packaging	<b>Reel Size</b>
JLE15ULD2-2	3000/Tape & Reel	7 inch

# **JLE15ULD2 -2**

# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

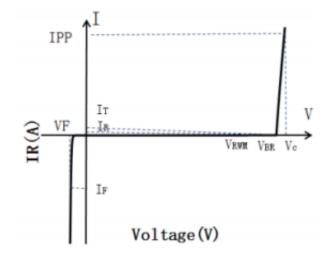
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20µs)	Ppk	320	W	
Peak Pulse Current (8/20µs)	IPP	10	А	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	v ESD	±30		
Operating Temperature Range	TJ	-55 to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

# **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise specified)**

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				15	V
Breakdown Voltage	VBR	$I_T = 1mA$ , Pin 1 to Pin 2	16.5			V
Reverse Leakage Current	I <sub>R</sub>	$V_{RWM} = 15V$			0.2	uA
Clamping Voltage	Vc	$I_{PP} = 1A (8 \times 20 \mu s \text{ pulse}), Pin$ 1 to Pin 2			21	V
Clamping Voltage	Vc	IPP = $10A (8 \times 20\mu \text{s pulse})$ , Pin 1 to Pin 2			32	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz		50		pF

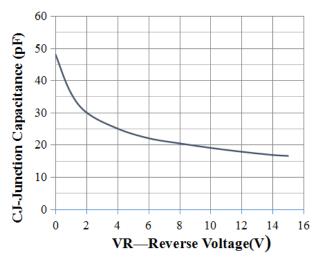
# **Portion Electronics Parameter**

Symbol	Parameter		
Іт	Test Current		
Ірр	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @Ic		

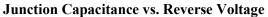


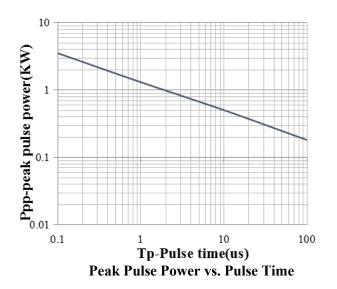
# JLE15ULD2-2

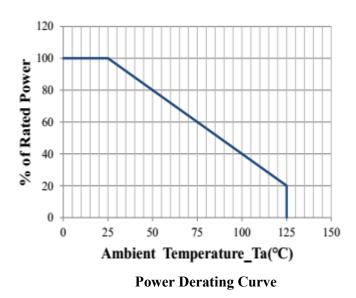


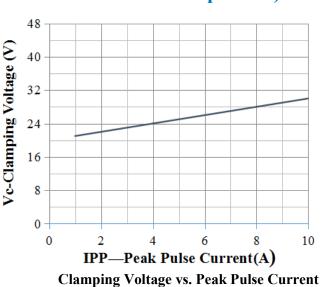


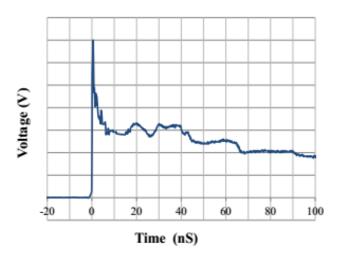
### Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise Specified)



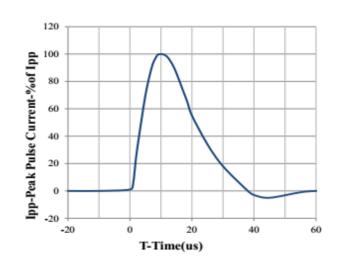








IEC61000-4-2 Pulse Waveform

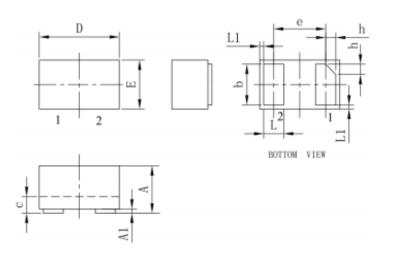


8 X 20us Pulse Waveform



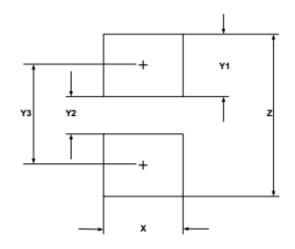
# JLE15ULD2-2

### **DFN1006-2** Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS						
	MI	MILLIMETERS			INCHES		
SYM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.45	0.50	0.55	0.018	0.020	0.022	
A1	0.00	0.02	0.05	0.000	0.001	0.002	
b	0.45	0.50	0.55	0.018	0.020	0.022	
с	0.12	0.15	0.18	0.005	0.006	0.007	
D	0.95	1.00	1.05	0.037	0.039	0.041	
е	0.65 BSC		;	0.026 BSC			
Е	0.55	0.60	0.65	0.022	0.024	0.026	
L	0.20	0.25	0.30	0.008	0.010	0.012	
L1	0.05REF			(	0.002RE	=	
h	0.07	0.12	0.17	0.003	0.005	0.007	

### **Suggested Land Pattern**



SYM	DIMENSIONS				
311	MILLIMETERS	INCHES			
х	0.60	0.024			
Y1	0.50	0.020			
Y2	0.30	0.012			
Y3	0.80	0.032			
Z	1.30	0.052			

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