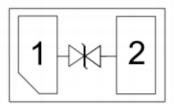


Description

The JLS05BGD6-2 is a Bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLS05BGD6-2 complies with the IEC 61000-4-2 (ESD) standard with ±30kV air and ±30kV contact discharge. It is assembled into an ultra small 1.6x1.0x0.5mm lead -free DFN pack-age. The small size and high ESD surge protection make JLS05BGD6-2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

58P:Device Marking Code

Features

- * 1800W peak pulse power (8/20μs)
- * Low leakage: nA
- * Operating voltage: 5V
- * Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 130A (8/20μs)
- RoHS Compliant
- * Package: DFN1610-2

Applications

- * Fast-charge battery chargers
- * Power management system
- * Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- Digital Cameras

Ordering Information

Part Number	Packaging	Reel Size
JLS05BGD6-2	3000/Tape & Reel	7 inch



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

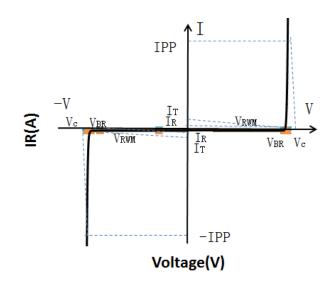
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20μs)	Ppk	1800	W	
Peak Pulse Current (8/20μs)	IPP	130	A	
ESD per IEC 61000-4-2 (Air)	r) VECD		kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30	K V	
Operating Temperature Range	TJ	-55 to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				5	V
Breakdown Voltage	VBR	$I_T = 1 \text{mA}$	5.2	5.5	6.5	V
Reverse Leakage Current	I_R	$V_{RWM} = 5V$			0.5	μΑ
Clamping Voltage	Vc	IPP = $1A (8 \times 20 \mu s \text{ pulse})$			5.5	V
Clamping Voltage	Vc	$I_{PP} = 130A (8 \times 20 \mu s \text{ pulse})$			14	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz		280	330	pF

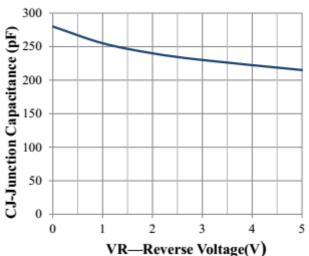
Portion Electronics Parameter

Symbol	Parameter		
IT	Test Current		
Ірр	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @Ic		

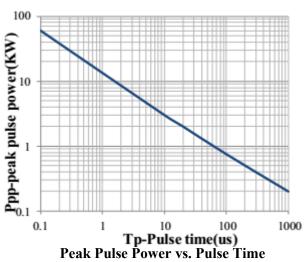


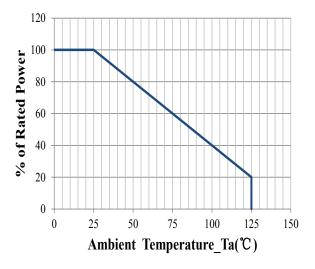


Typical Performance Characteristics (T_A=25°C unless otherwise Specified)

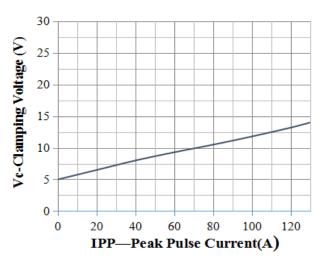


Junction Capacitance vs. Reverse Voltage

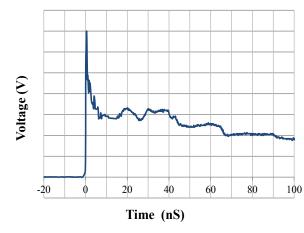




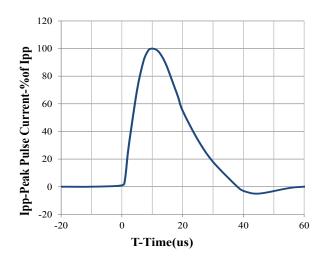
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



IEC61000-4-2 Pulse Waveform

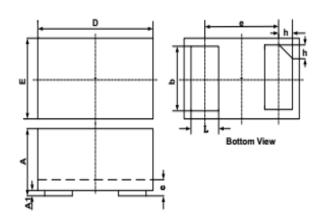


8 X 20us Pulse Waveform

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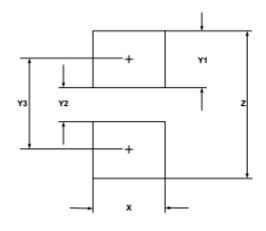


DFN1610-2 Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS					
0./14	MILLIMETERS		INCHES			
SYM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.75	0.80	0.85	0.030	0.032	0.034
С	0.10	0.15	0.20	0.004	0.006	0.008
D	1.55	1.60	1.65	0.062	0.064	0.066
е	1.10 BSC		0	.044 BS0	С	
Е	0.95	1.00	1.05	0.038	0.040	0.042
L	0.35	0.40	0.45	0.014	0.016	0.018
h	0.15	0.20	0.25	0.006	0.008	0.010

Suggested Land Pattern



SYM	DIMENSIONS			
STW	MILLIMETERS	INCHES		
Х	1.00	0.040		
Y1	0.62	0.025		
Y2	0.60	0.024		
Y3	1.22	0.049		
Z	1.85	0.074		

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