



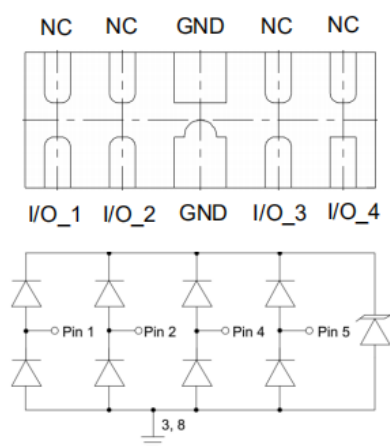
Description

The JLE33URD4-10 is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The JLE33URD4-10 has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) with $\pm 25\text{kV}$ air and $\pm 20\text{kV}$ contact discharge. It is assembled into a 10-pin $2.5 \times 1.0 \times 0.5\text{mm}$ lead-free DFN package. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines such as USB 3.0 and HDMI. The small size, ultra-low capacitance and high ESD surge protection make JLE33URD4-10 an ideal choice to protect HDMI, MDDI, USB

Features

- * 80W peak pulse power (8/20 μs)
- * Low leakage : nA level
- * Low operating voltage: 3.3V
- * Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 25\text{kV}$
 - Contact discharge: $\pm 20\text{kV}$
 - IEC61000-4-5 (Lightning) 5A (8/20 μs)
- * Package: DFN2510-10

Circuit Diagram

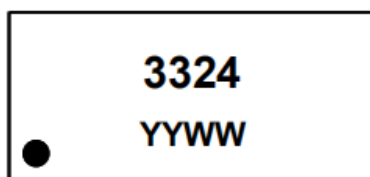


Circuit and Pin Schematic

Applications

- * HDMI 1.3&1.4, USB 2.0 & 3.0 and MDDI ports
- * Monitors and flat panel displays
- * Set-top box and Digital TV
- * Video graphics cards
- * Digital Visual Interfaces (DVI)
- * Notebook Computers
- * PCI Express and Serial SATA

Marking Diagram



Transparent top view

3323YYWW: Device Marking Code

Ordering Information

Part Number	Packaging	Reel Size
JLE33URD4-10	3000/Tape & Reel	7 inch



JLE33URD4-10

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	80	W
Peak Pulse Current (8/20 μs)	IPP	5	A
ESD per IEC 61000-4-2 (Air)	VESD	± 25	kV
ESD per IEC 61000-4-2 (Contact)		± 22	
Operating Temperature Range	TJ	-55to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

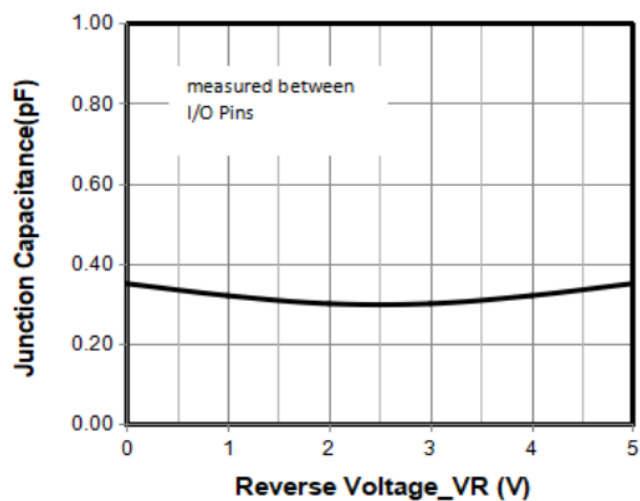
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}	Any I/O pin to ground			3.3	V
Breakdown Voltage	V_{BR}	$I_{\text{T}} = 1\text{mA}$, any I/O pin to ground	3.5			V
Reverse Leakage Current	I_{R}	$V_{\text{RWM}} = 3.3\text{V}$, any I/O pin to ground		0.01	0.5	μA
Clamping Voltage	V_{C}	$I_{\text{PP}} = 1\text{A}$ (8 x 20 μs pulse), any I/O pin to ground			9	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 5\text{A}$ (8 x 20 μs pulse), any I/O pin to ground			16	V
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$, between I/O pins		0.3	0.4	pF
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$, any I/O pin to ground			0.8	pF

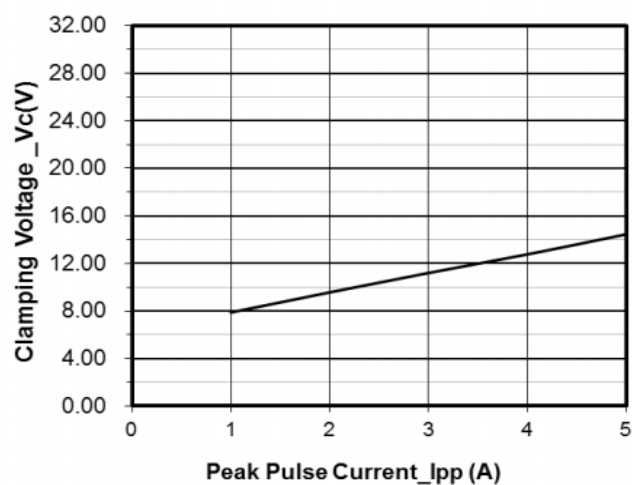


JLE33URD4-10

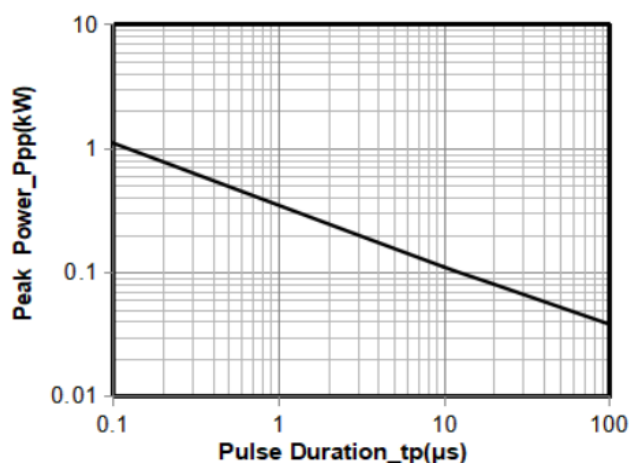
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



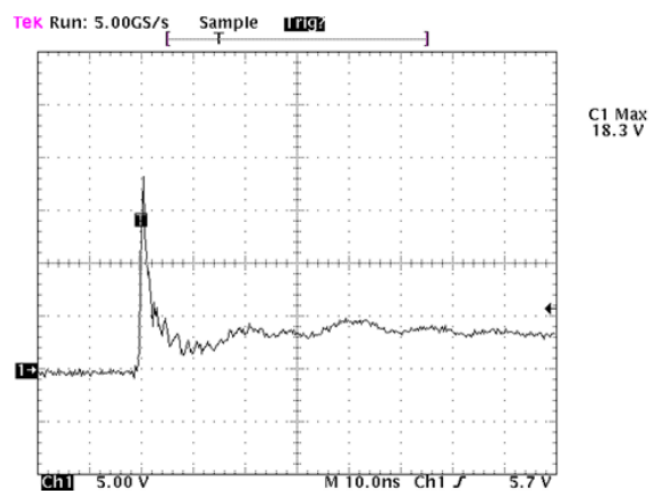
Junction Capacitance vs. Reverse Voltage



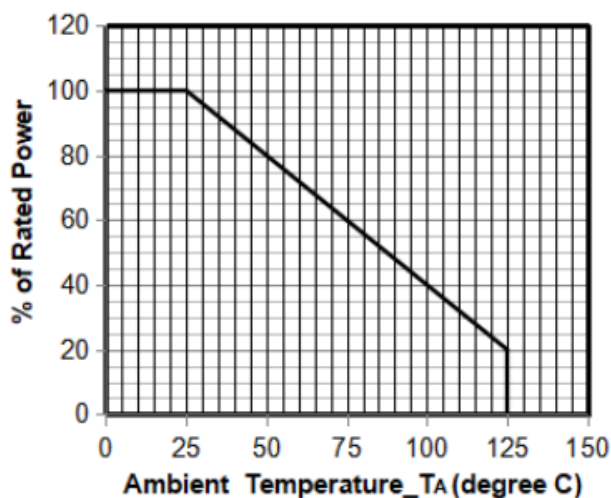
Clamping Voltage vs. Peak Pulse Current



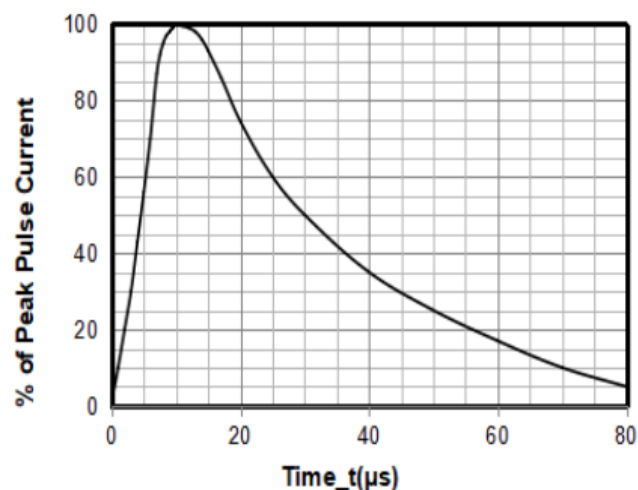
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

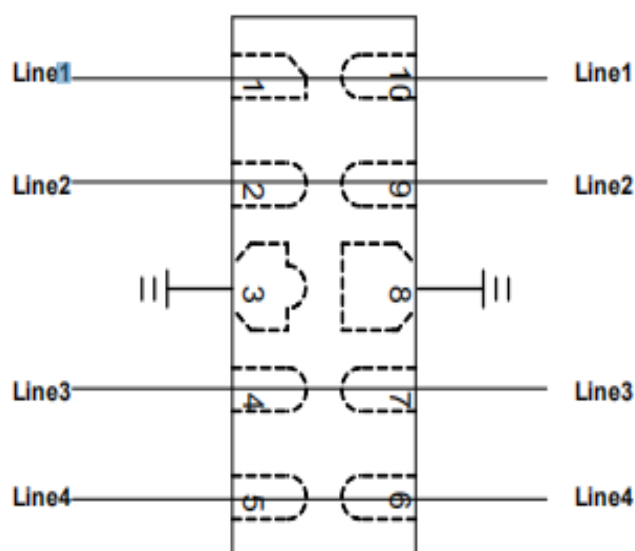


8 X 20us Pulse Waveform

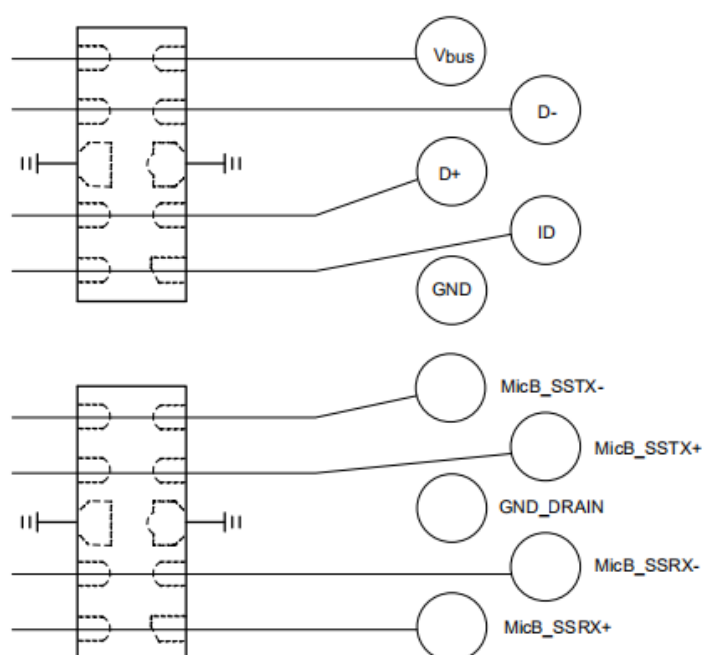


JLE33URD4-10

The JE33U4RD40-10 is designed for easy PCB layout by allowing the traces to run straight through the device. The PCB traces could be used to connect the pin pairs for each line. For example, line 1 enters at pin 1 and exits at pin 10 and the PCB trace connects Pin 1 and Pin 10 together. Ground is connected at Pin 3 and Pin 8.



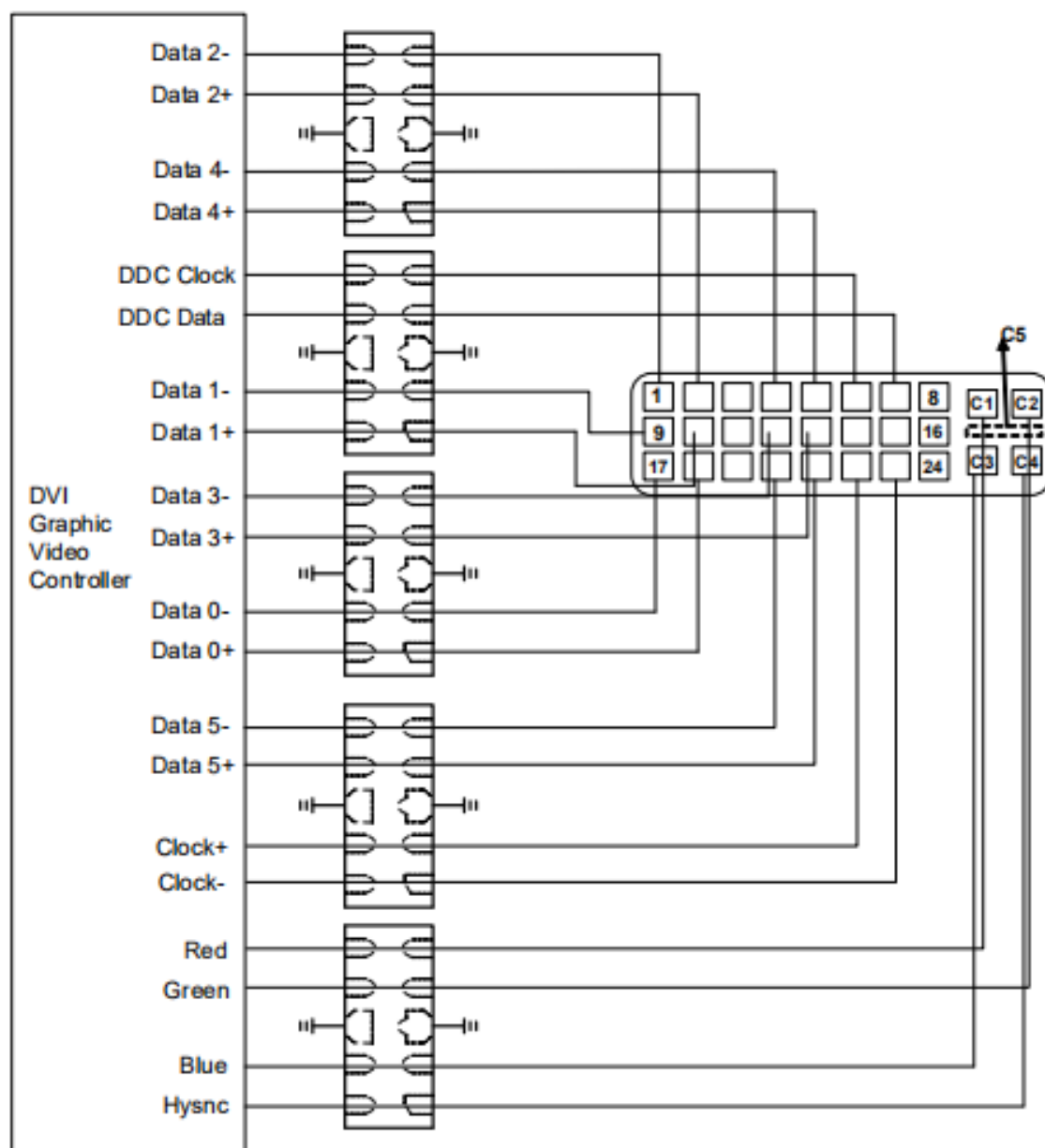
JE33U4RD40-10 on USB 3.0 Port Application





JLE33URD4-10

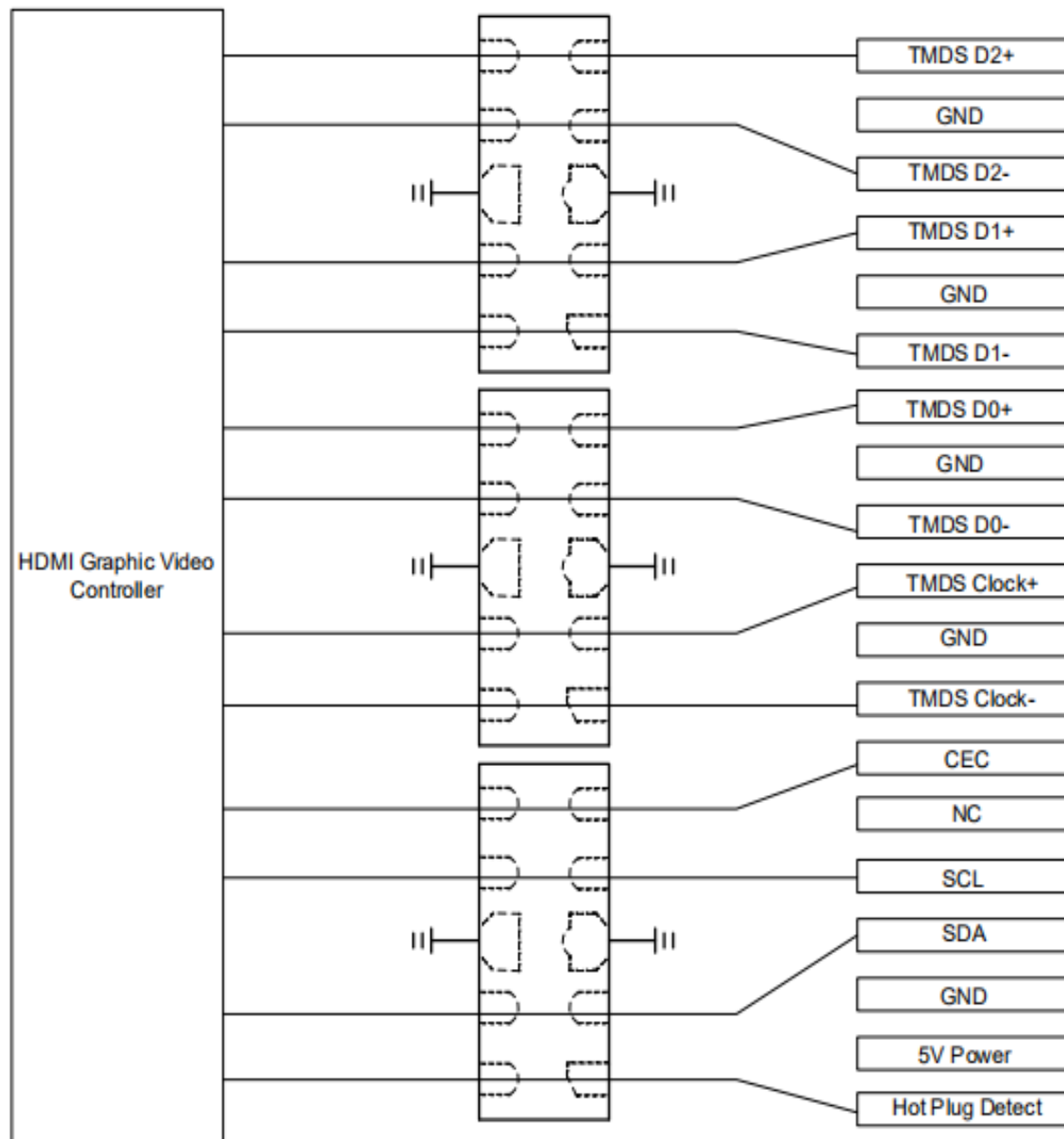
JE33U4RD4-10 on DVI Port Application





JLE33URD4-10

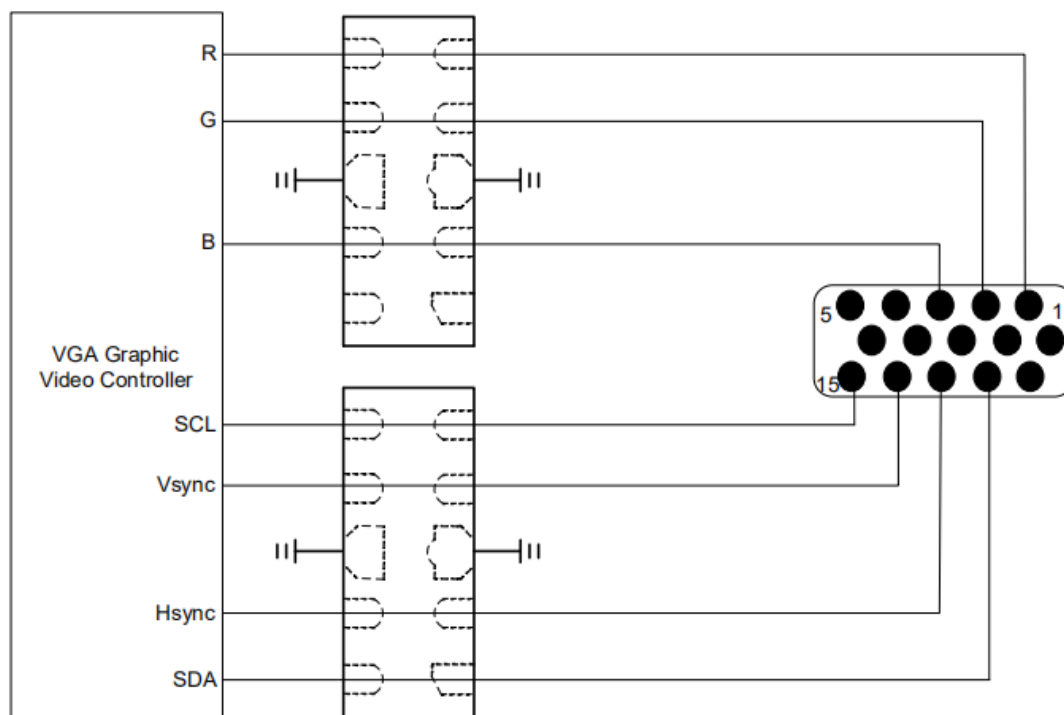
JLE33URD4 -10 on HDMI Port Application



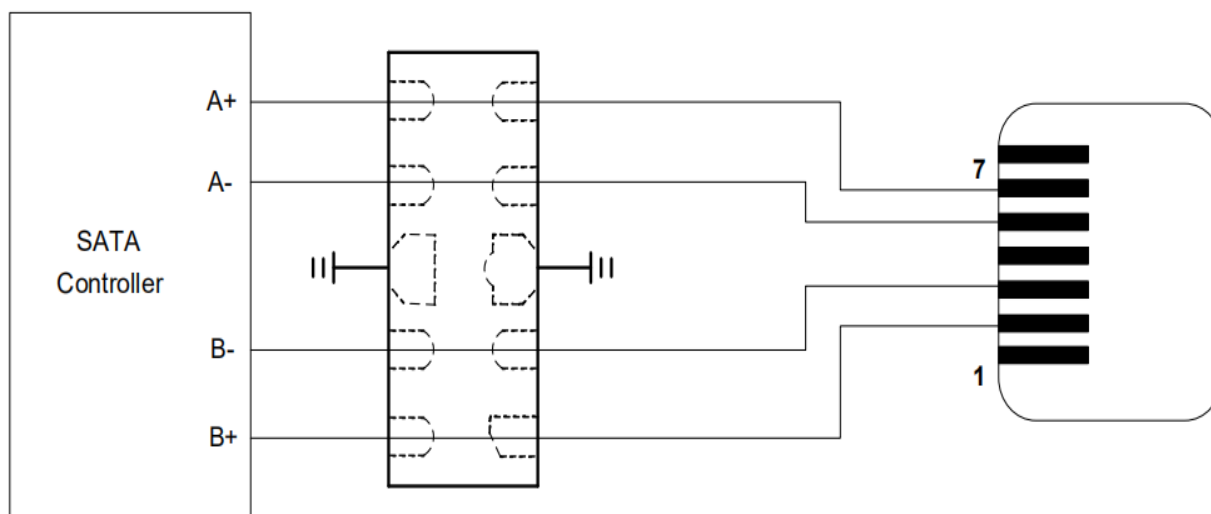


JLE33URD4-10

JLE33URD4 -10 on VGA Port Application



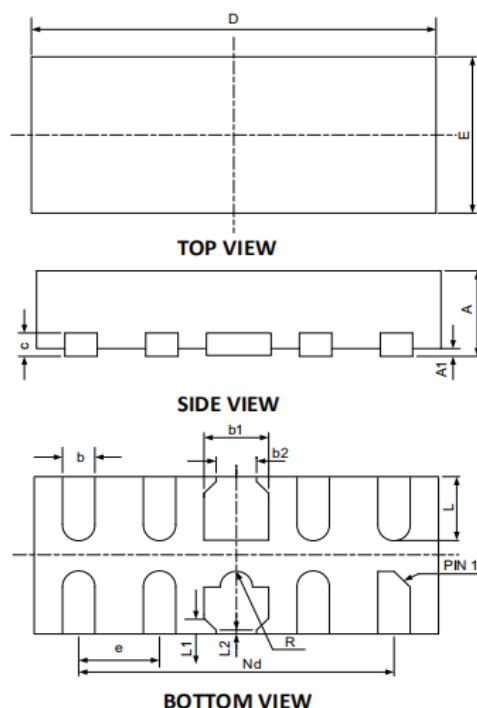
JE33U4RD40-10 on eSATA Port Application





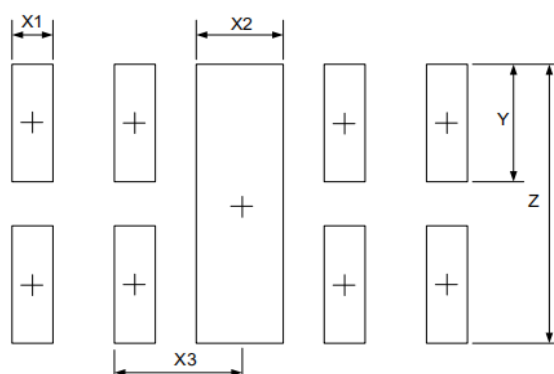
JLE33URD4-10

DFN2510-10 Package Outline Drawing (Dimensions in millimeters)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.15	0.20	0.25	0.006	0.008	0.010
b1	0.35	0.40	0.45	0.014	0.016	0.018
b2	0.20	0.25	0.30	0.008	0.010	0.012
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.45	2.50	2.55	0.098	0.100	0.102
e	0.50BSC			0.020BSC		
Nd	2.00BSC			0.080BSC		
E	0.95	1.00	1.05	0.038	0.040	0.042
L	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.075REF			0.003REF		
L2	0.050REF			0.002REF		
h	0.08	0.12	0.15	0.003	0.005	0.006
R	0.05	0.10	0.15	0.002	0.004	0.006

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X1	0.200	0.008
X2	0.400	0.016
X3	0.500	0.020
Y	0.600	0.024
Z	1.400	0.056

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