



JLE33BGS3-2

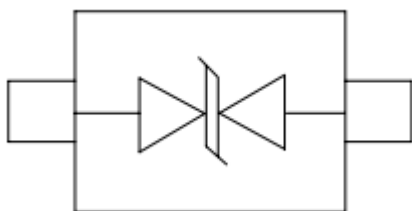
1-Line Bi-directional High Power TVS Diode

Jialan-Microelectronics

Description

The JLE33BGS3-2 is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers and PDA's, using monolithic silicon technology to provide fast response time and ultra low ESD clamping voltage, making this device an ideal solution for protecting sensitive semiconductor components from damage. The JLE33BGS3-2 complies with the IEC 61000-4-2 (ESD) with $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. The JLE33BGS3-2 is assembled into a lead-free SOD-323 package and will protect one bidirectional line.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

33L: Device Marking Code

Features

- * 330W peak pulse power (8/20us)
- * Low leakage: μA level
- * Operating voltage: 3.3V
- * Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
- * RoHS Compliant
- * Package: SOD-323
- * Lead Finish: Matte Tin

Applications

- * Fast-charge battery chargers
- * Power management system
- * Cellular Handsets and Accessories
- * Personal Digital Assistants
- * Notebooks and Handhelds
- * Portable Instrumentation
- * Digital Cameras

Ordering Information

Part Number	Packaging	Reel Size
JLE33BGS3-2	3000/Tape & Reel	7 inch



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Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

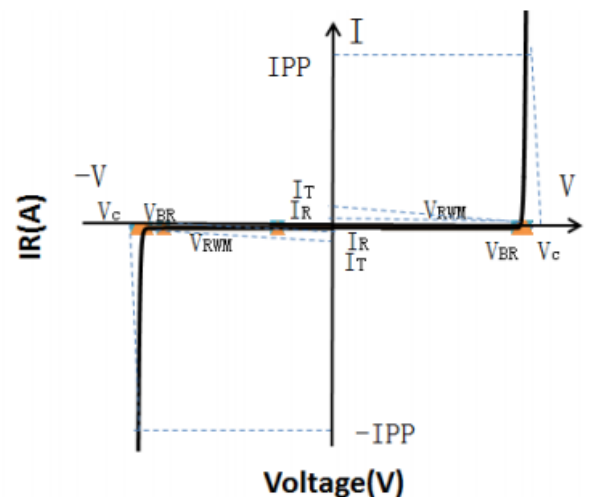
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	330	W
Peak Pulse Current (8/20 μs)	IPP	25	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				3.3	V
Breakdown Voltage	V_{BR}	$I_{\text{T}} = 1\text{mA}$	3.8			V
Reverse Leakage Current	I_{R}	$V_{\text{RWM}} = 3.3\text{V}$			1.0	μA
Clamping Voltage	V_{C}	$I_{\text{PP}} = 1\text{A}$ (8 x 20 μs pulse)			6	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 25\text{A}$ (8 x 20 μs pulse)			12	V
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$			100	pF

Portion Electronics Parameter

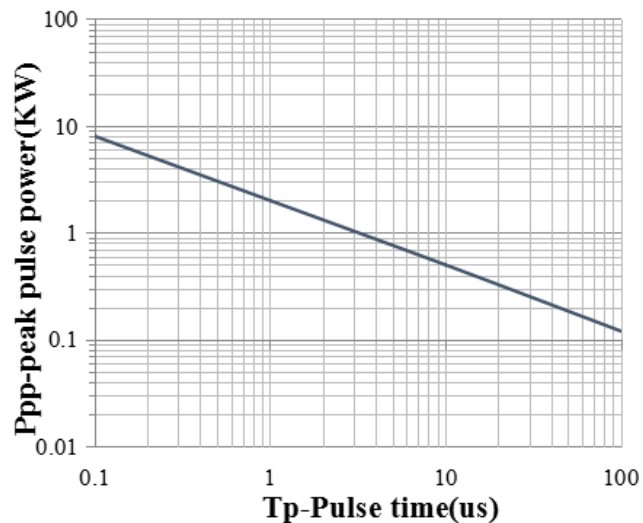
Symbol	Parameter
I_{T}	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_{C}	Clamping Voltage @ I_{C}



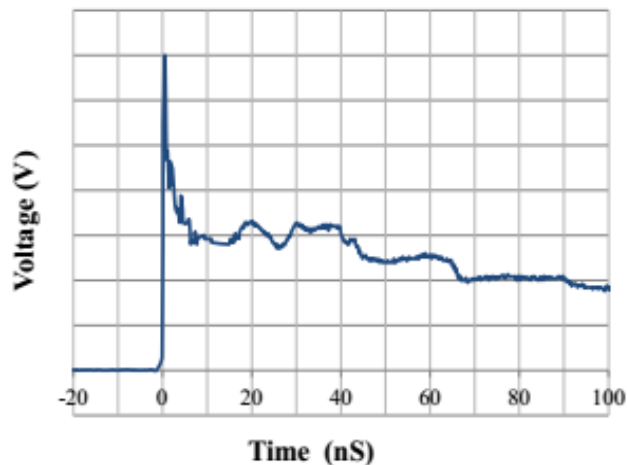


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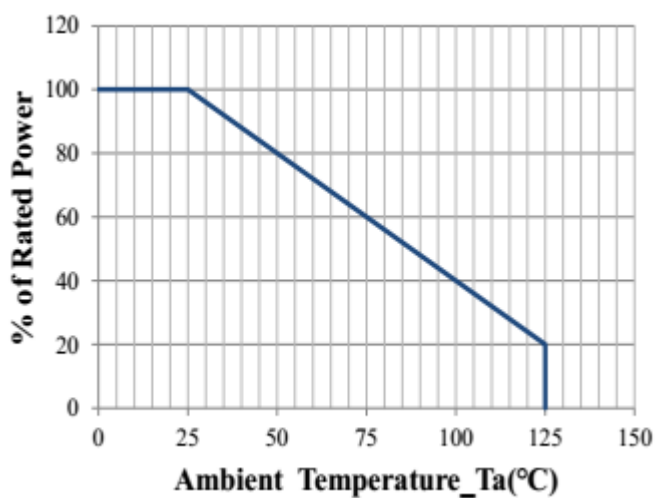
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



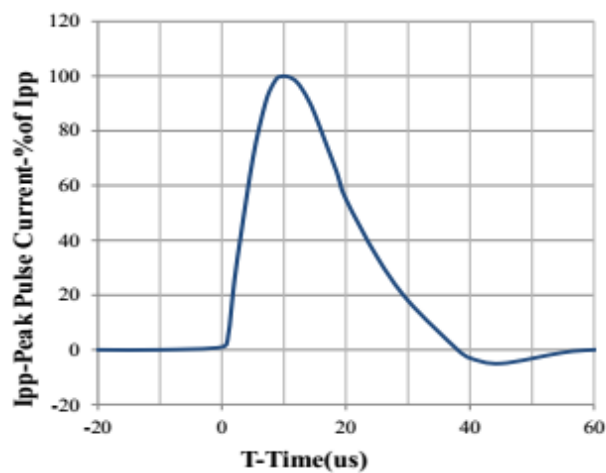
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

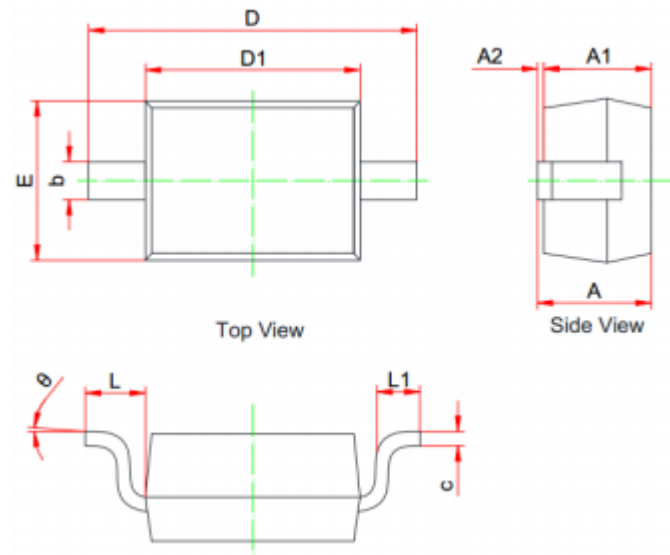


8 X 20us Pulse Waveform



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SOD-323 Package Outline Drawing (Dimensions in millimeters)



SYM	MILLIMETERS		
	MIN	NOM	MAX
A	0.800	--	1.100
A1	0.800	--	0.900
A2	0.000	--	0.100
b	0.250	--	0.400
c	0.080	--	0.177
D1	1.600	1.700	1.800
D	2.300	--	2.800
E	1.150	--	1.400
L	0.475REF		
L1	0.100	--	0.500
Θ	0°	--	8°

Suggested Land Pattern



Unit: mm

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